			and
Notice of Allowability	Application No.	Applicant(s)	
	10/629,167	NGUYEN ET AL.	
	Examiner	Art Unit	
	Daniel D. Chang	2819	
The MAILING DATE f this communication appears on the cover sheet with the corresp ndence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. 🖾 This communication is responsive to <u>Amendment filed April 5, 2004</u> .			
2. ☑ The allowed claim(s) is/are <u>27-38</u> .			
3. $igotimes$ The drawings filed on <u>28 July 2003</u> are accepted by the Exa	aminer.		
4.			
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 4/8/04 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	5. Notice of Informal Page 6. Interview Summary Paper No./Mail Date 7. Examiner's Amendm 8. Examiner's Stateme 9. Other	(PTO-413), e nent/Comment	·

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Acknowledgement

Receipt is acknowledged of the Amendment filed April 5, 2004.

Reasons for Allowance

Claims 27-38 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Tsugaru, taken alone or in combination of other references, does not teach or fairly suggest a level-shifting input receiver circuit or a method for level-shifting a received signal comprising, among other things, differentially comparing the first and second input signals to generate a differential current signal and providing a resistive load coupled in series with a pulling current source between an internal power supply voltage and a ground supply voltage (claim 27); and driving an NMOS differential pair with the first and second input signals to obtain an intermediate differential current signal; and reflecting the intermediate differential current signal through a PMOS current mirror pair configured to draw current from the external power supply voltage in order to generate the differential current signal (claim 36), as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

> Daniel D. Chang Primary Examiner

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DANIEL CHANG PRIMARY EXAMINER

DC